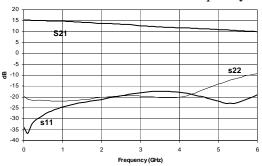


Product Description

Sirenza Microdevices' SBA-4086 is a high performance InGaP/GaAs Heterojunction Bipolar Transistor MMIC Amplifier. A Darlington configuration designed with InGaP process technology provides broadband performance up to 5 GHz with excellent thermal perfomance. The heterojunction increases breakdown voltage and minimizes leakage current between junctions. Cancellation of emitter junction non-linearities results in higher suppression of intermodulation products. Only a single positive supply voltage, DC-blocking capacitors, a bias resistor, and an optional RF choke are required for operation.

The matte tin finish on Sirenza's lead-free package utilizes a post annealing process to mitigate tin whisker formation and is RoHS compliant per EU Directive 2002/95. This package is also manufactured with green molding compounds that contain no antimony trioxide nor halogenated fire retardants.

Gain and Return Loss vs Frequency



SBA-4086

SBA-4086Z Po RoHS Compliant & Green Package

DC-5 GHz, Cascadable InGaP/GaAs HBT MMIC Amplifier



Product Features

- Now available in Lead Free, RoHS Compliant, & Green Packaging
- IP3 = 33.5dBm @ 1950MHz
- Pout=12.3dBm @-45dBc ACP IS-95 1950MHz
- Robust 1000V ESD, Class 1C
- Operates From Single Supply
- Patented Thermal Design

Applications

- PA Driver Amplifier
- Cellular, PCS, GSM, UMTS
- IF Amplifier
- Wireless Data, Satellite Terminals

 $Z_s = Z_t = 50 \text{ Ohms}$

| Symbol | Parameter | U n its | Frequency | M in . | Тур. | Мах. |
|--|---|---------|---------------------|--------------|------------------|--------------|
| G | G Small Signal Gain | | 850 MHz 1950 MHz | 13.3 12.7 | 1 4 .8 1 4 .2 | 16.3 15.7 |
| P _{1dB} | P _{1dB} Output Power at 1dB Compression | | 850 MHz 1950 MHz | 17.5 | 19.1 19.0 | |
| O IP ₃ | OIP 3 Output Third Order Intercept Point | | 850 MHz 1950 MHz | 31.5 | 36.5 33.5 | |
| Pout Output Power @ -45dBc ACP IS-95 9 Forward Channels | | d B m | 1950 MHz | | 12.3 | |
| Bandwidth | Bandwidth Determined by Return Loss (>10dB) IR L Input Return Loss OR L Output Return Loss NF Noise Figure | | | | 5000 | |
| IR L | | | 1950 MHz | 14.0 | 21.0 | |
| ORL | | | 1950 MHz | 14.0 | 20.5 | |
| NF | | | 1950 MHz | | 4.8 | 5 .8 |
| V _D Device Operating Voltage | | V | | 4 .6 | 5.0 | 5 .4 |
| I _D | I _D Device Operating Current | | | 72 | 8 0 | 8 8 |
| R _{TH} , j-I | Thermal Resistance (junction to lead) | °C/W | | | 102 | |

Performance tests and ratings for Sirenza Microdevices' products were performed internally by Sirenza and measured using specific computer systems and/or and components and reflect the approximate performance of the products as measured by those tests. Any difference in circuit implementation, test software or test equipment may affect actual performance. The information provided herein is believed to be reliable at press time and Sirenza Microdevices assumes no responsibility for the user of this information. All such use shall be entirely at the user's own risk. Prices and specifications for Sirenza Microdevices' products are subject to change without notice. Buyers should consult Sirenza Microdevices' standard terms and conditions of sale for Sirenza's limited warranty with regard to its products. No patent rights or licenses to any of the circuits described herein are implied or granted to any third party. Sirenza Microdevices does not authorize or warrant any product for use in life-support devices and/or systems.

 $I_D = 80 \text{ mA Typ.}$

 $T_1 = 25^{\circ}C$

 $V_{s} = 8 \ V$

 $R_{BIAS} = 39 \text{ Ohms}$

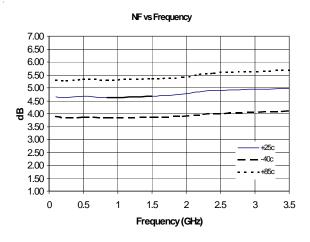
Test Conditions:

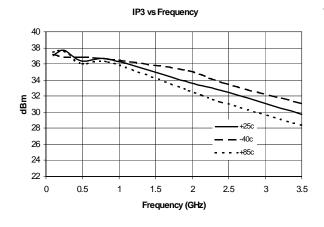
OIP₃ Tone Spacing = 1 MHz, Pout per tone = 0 dBm



Typical RF Performance at Key Operating Frequencies

| | | Frequency (MHz) | | | | | | |
|------------------|---|-----------------|------------------|------|------------------------|-----------|---------------|---------|
| Symbol | Parameter | Unit | 100 | 500 | 850 | 1950 | 2400 | 3500 |
| G | Small Signal Gain | dB | 15.2 | 15.0 | 14.8 | 14.2 | 12.4 | 12.1 |
| OIP ₃ | Output Third Order Intercept Point | dBm | 37.1 | 36.3 | 36.5 | 33.5 | 32.7 | 29.7 |
| P _{1dB} | Output Power at 1dB Compression | dBm | 19.0 | 19.1 | 19.1 | 19.0 | 18.3 | 16.4 |
| IRL | Input Return Loss | dB | 36 | 28 | 25 | 21 | 19.7 | 17 |
| ORL | Output Return Loss | dB | 21 | 21 | 21.0 | 20.5 | 19.6 | 20.2 |
| S ₁₂ | Reverse Isolation | dB | 18 | 18 | 18 | 18 | 19 | 20 |
| NF | Noise Figure | dB | 4.7 | 4.7 | 4.6 | 4.8 | 4.9 | 5.0 |
| Test | Test Conditions: $V_s = 8 \text{ V}$ $R_{\text{BIAS}} = 39 \text{ Ohms}$ | | 0 mA Typ. 5ºC | 3 | e Spacing = 50 Ohms | 1 MHz, Po | ut per tone : | = 0 dBm |



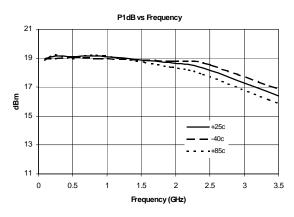


Absolute Maximum Ratings

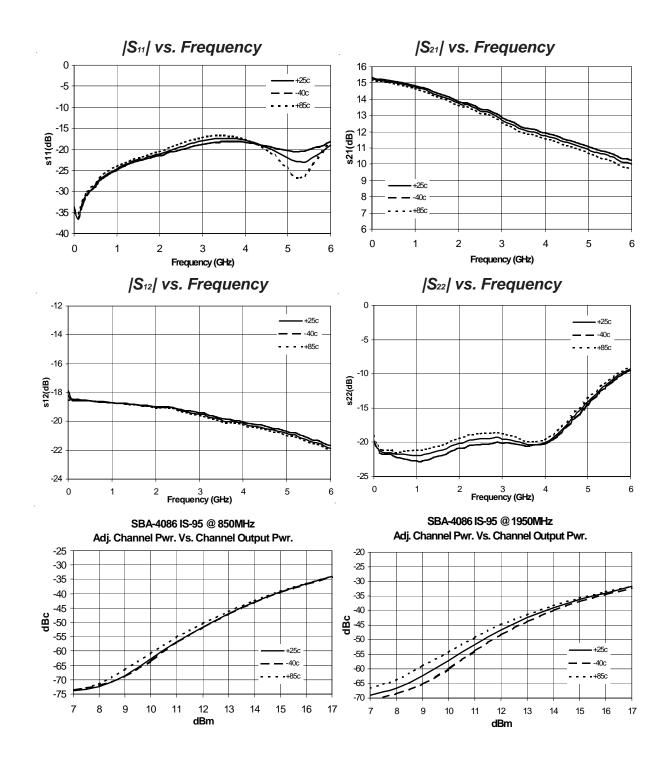
| Parameter | Absolute Limit |
|---|----------------|
| Max. Device Current (I _D) | 130 mA |
| Max. Device Voltage (V _D) | 6 V |
| Max. RF Input Power | +17 dBm |
| Max Operating Dissipated Power | 0.65 W |
| Max. Junction Temp. (T _J) | +150°C |
| Operating Temp. Range (T _L) | -40°C to +85°C |
| Max. Storage Temp. | +150°C |

Operation of this device beyond any one of these limits may cause permanent damage. For reliable continous operation, the device voltage and current must not exceed the maximum operating values specified in the table on page one.

Bias Conditions should also satisfy the following expression: $I_{D}V_{D}<(T_{J}-T_{L})\ /\ R_{TH},\ j-I\qquad T_{L}=T_{LEAD}$

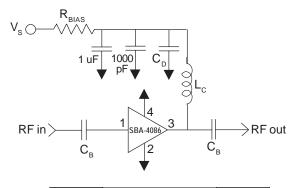


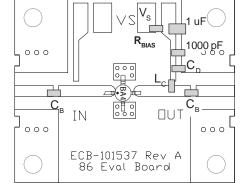






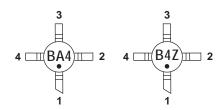
Basic Application Circuit





Part Identification Marking

The part will be marked with an "BA4" or "B4Z" designator on the top surface of the package.





Caution: ESD sensitive

Appropriate precautions in handling, packaging and testing devices must be observed.

Application Circuit Element Values

| D-f | | Frequency (Mhz) | | | | | |
|-------------------------|--------|-----------------|-------|-------|-------|--|--|
| Reference Designator | 500 | 850 | 1950 | 2400 | 3500 | | |
| C _B | 220 pF | 100 pF | 68 pF | 56 pF | 39 pF | | |
| C _D | 100 pF | 68 pF | 22 pF | 22 pF | 15 pF | | |
| L _c | 68 nH | 33 nH | 22 nH | 18 nH | 15 nH | | |

| Recommended Bias Resistor Values for I_D =80mA R_{BIAS} =(V_S - V_D) / I_D | | | | |
|--|-------|------|------|------|
| Supply Voltage(V _s) | 7.5 V | 8 V | 10 V | 12 V |
| R _{BIAS} | 33 Ω | 39 Ω | 68 Ω | 91 Ω |
| Note: P provides DC higs stability over temperature | | | | |

Mounting Instructions

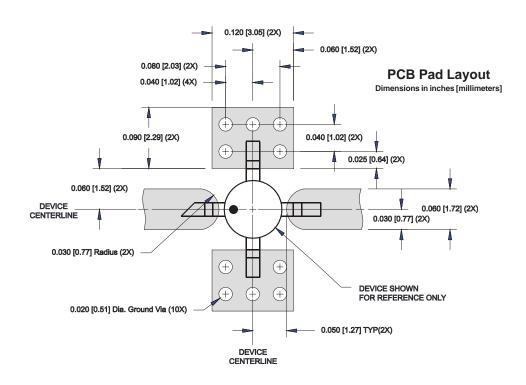
- 1. Use a large ground pad area under device pins 2 and 4 with many plated through-holes as shown.
- We recommend 1 or 2 ounce copper. Measurements for this data sheet were made on a 31 mil thick FR-4 board with 1 ounce copper on both sides.

| Pin# | Function | Description |
|------|-----------------|---|
| 1 | RF IN | RF input pin. This pin requires the use of an external DC blocking capacitor chosen for the frequency of operation. |
| 2, 4 | GND | Connection to ground. Use via holes for best performance to reduce lead inductance as close to ground leads as possible. |
| 3 | RF OUT/ BIAS | RF output and bias pin. DC voltage is present on this pin, therefore a DC blocking capacitor is necessary for proper operation. |

Part Number Ordering Information

| Part Number | Reel Size | Devices/Reel |
|-------------|-----------|--------------|
| SBA-4086 | 7" | 1000 |
| SBA-4086Z | 7" | 1000 |





Nominal Package Dimensions

Dimensions in inches [millimeters]
Refer to drawing posted at www.sirenza.com for tolerances.

